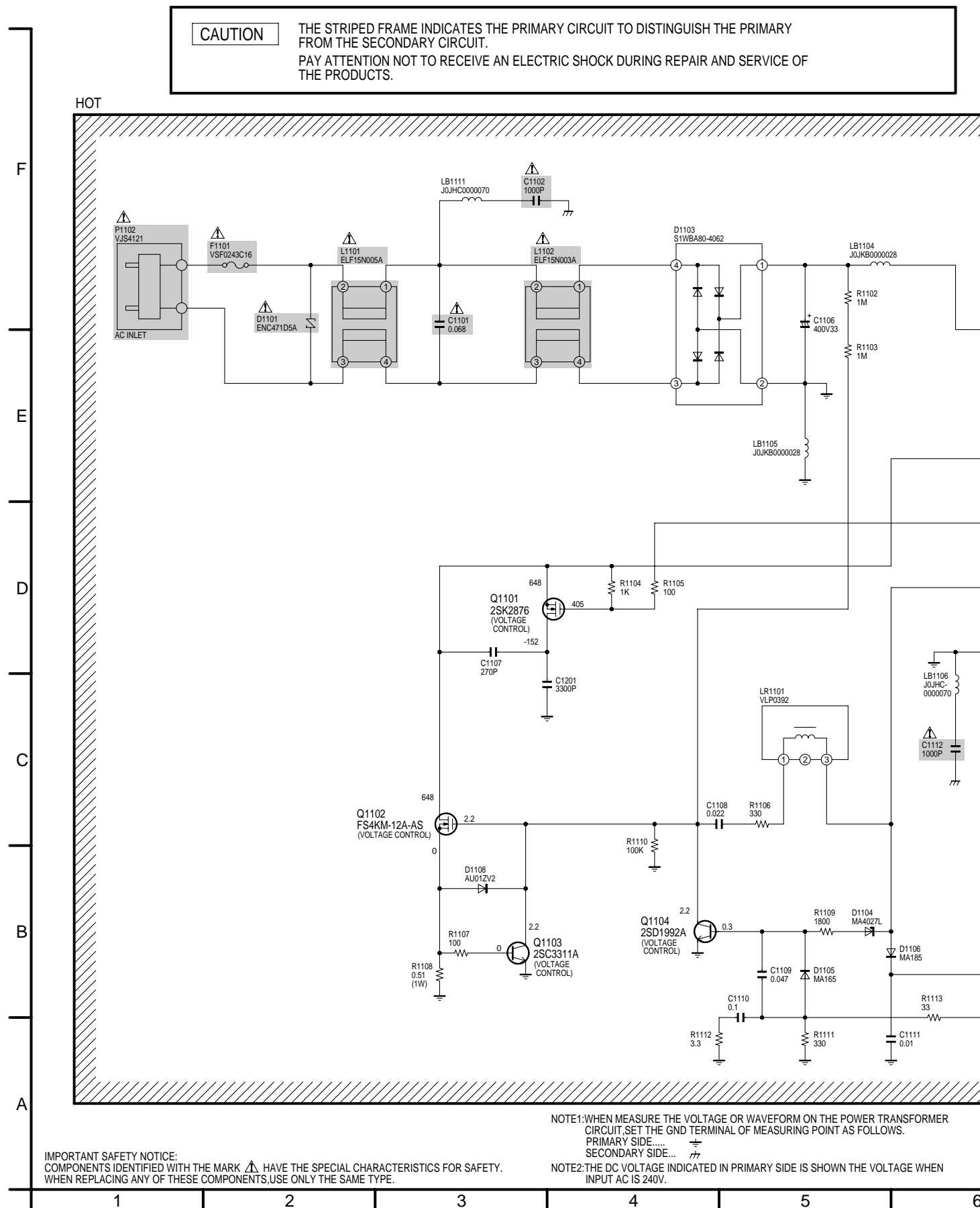
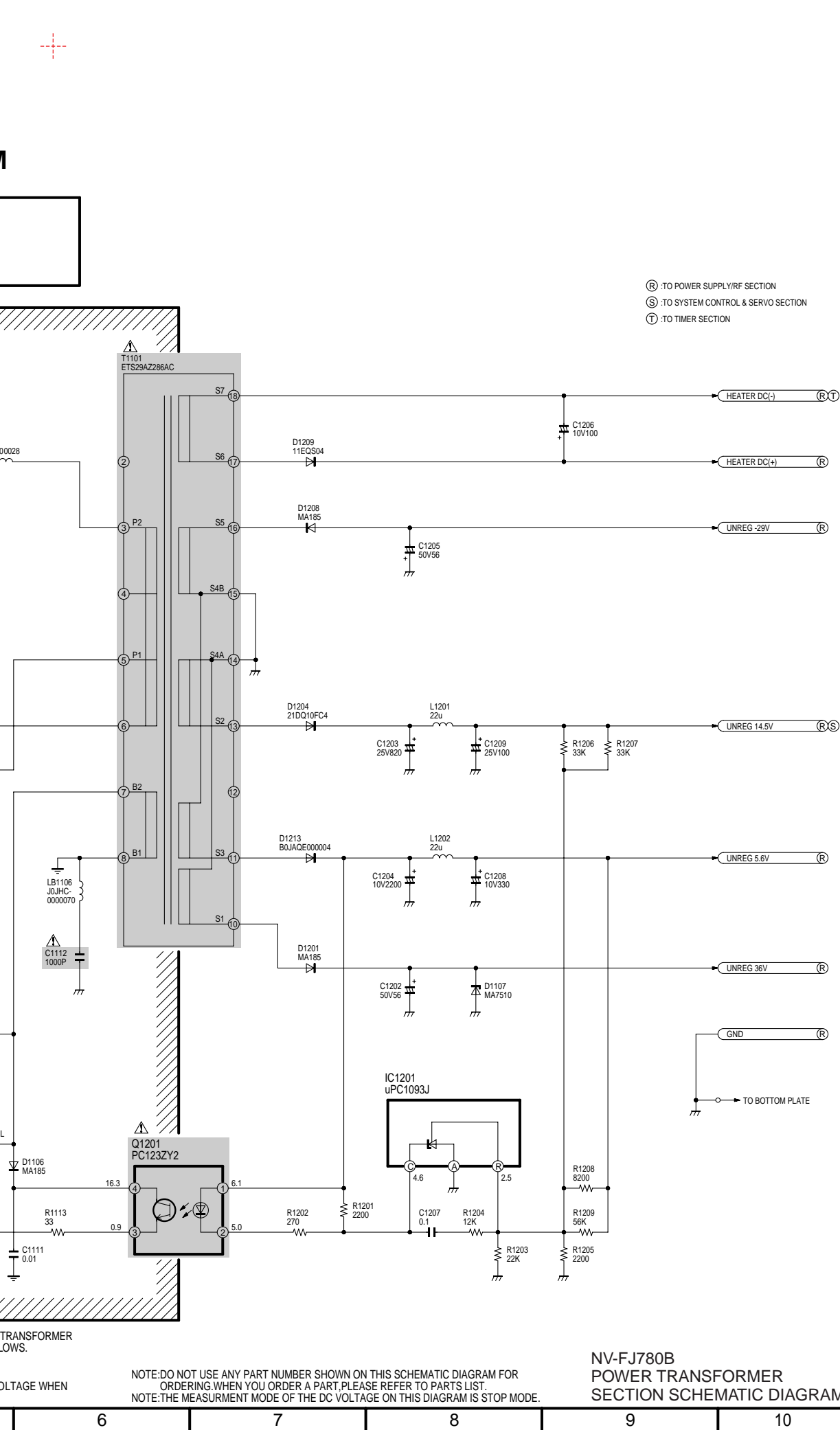


## 8 SCHEMATIC DIAGRAMS

### 8.1. POWER TRANSFORMER SECTION IN MAIN SCHEMATIC DIAGRAM



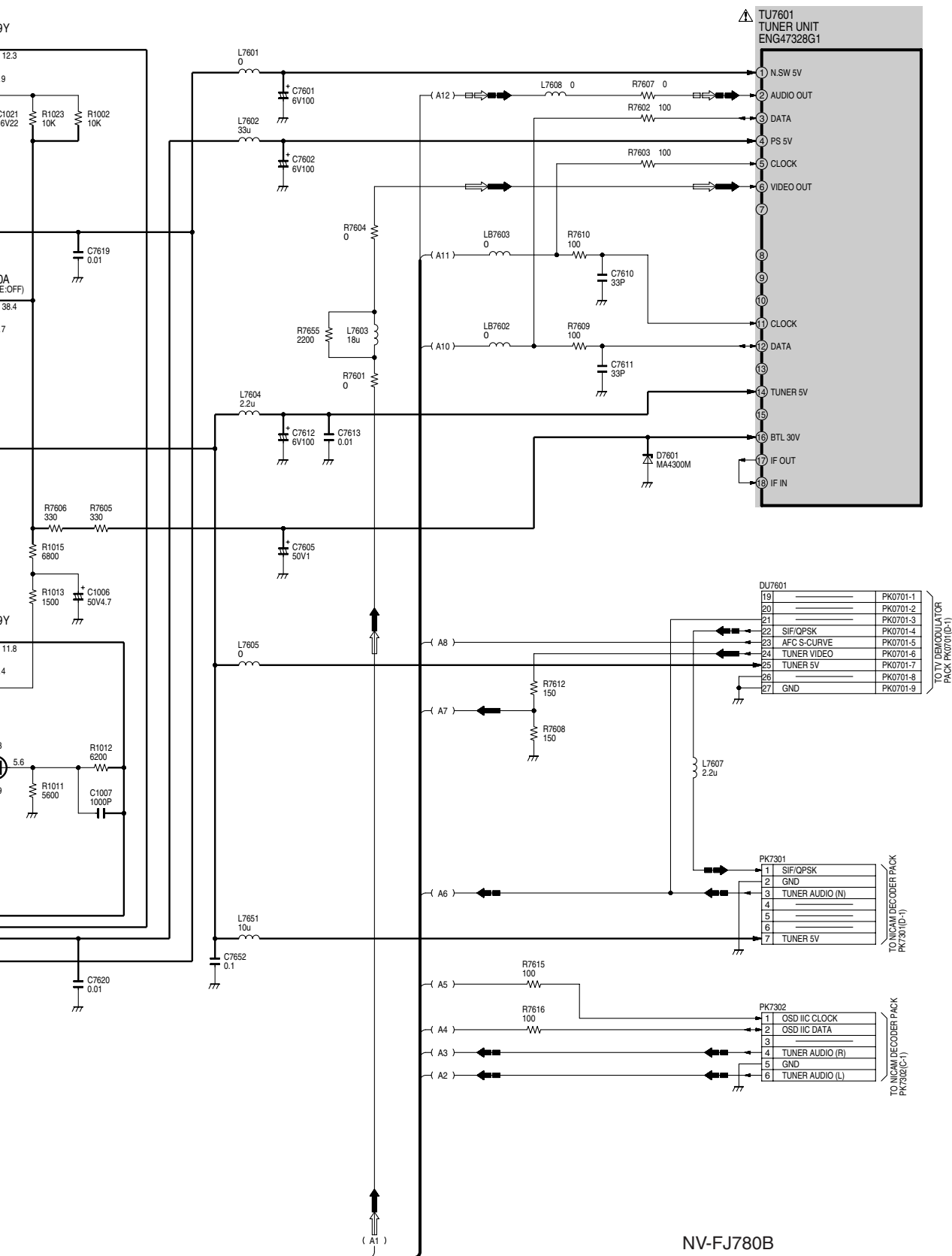




BACK MODE

←■ AUDIO MAIN SIGNAL PATH IN REC MODE

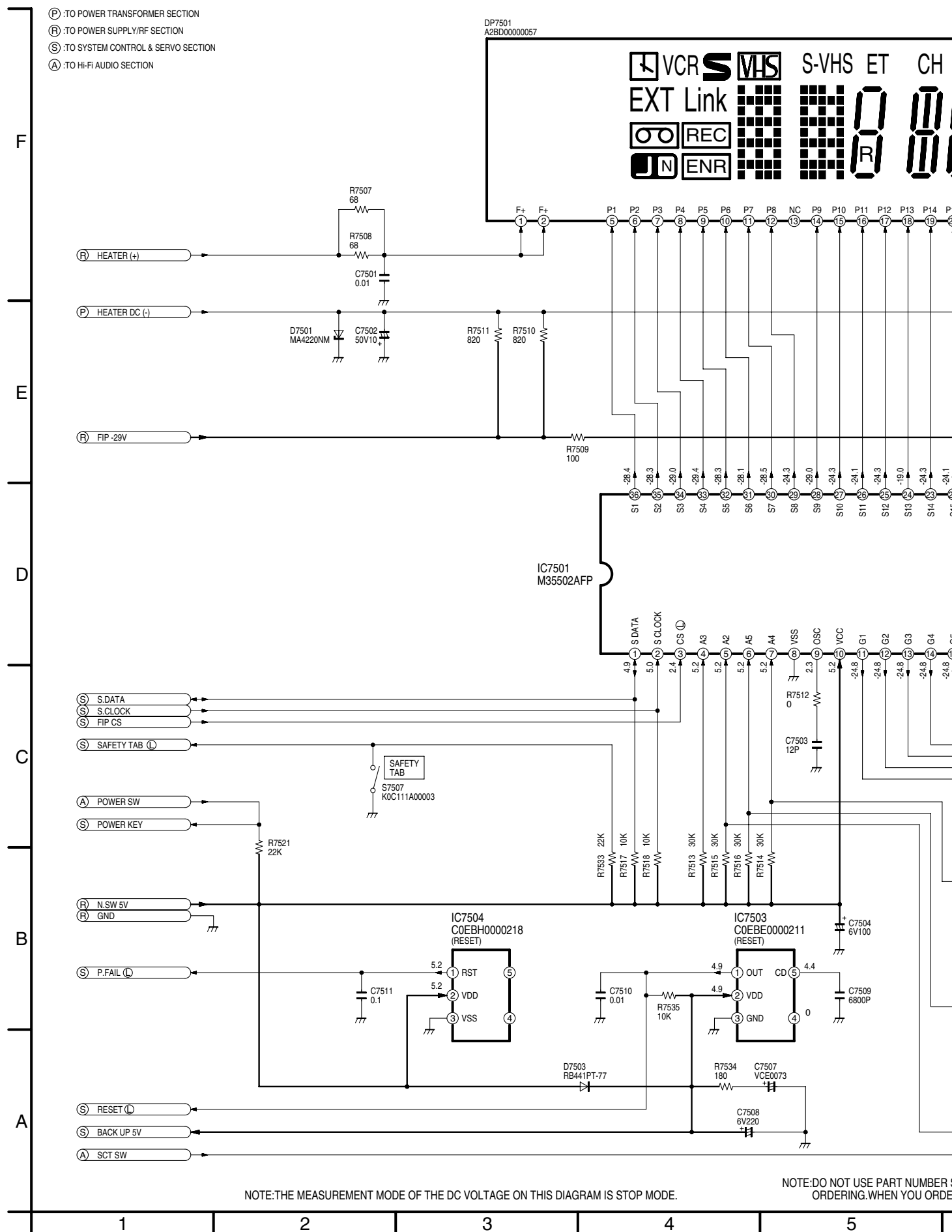
←□ AUDIO MAIN SIGNAL PATH IN PLAYBACK MODE

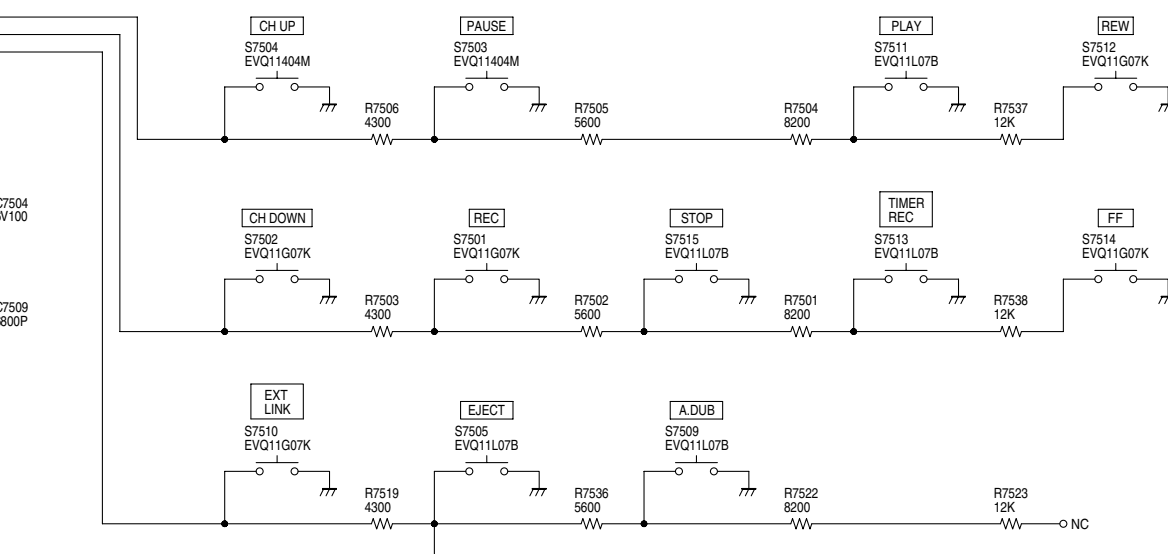
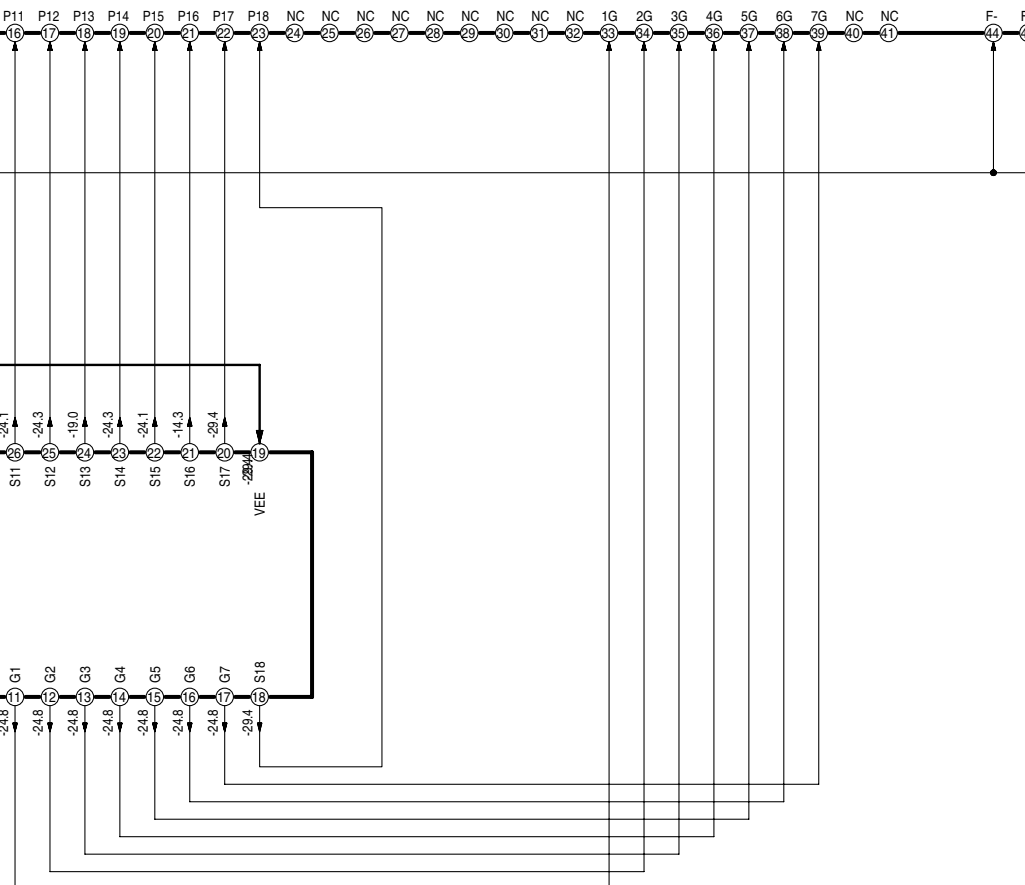
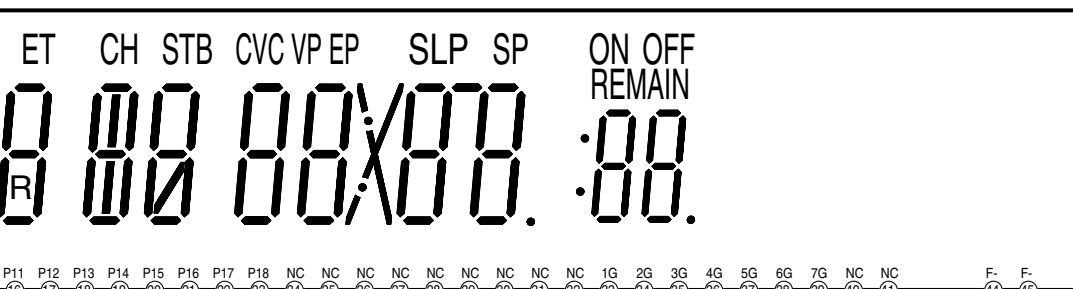


NOTE: DO NOT USE ANY PART NUMBER SHOWN ON THIS SCHEMATIC DIAGRAM FOR ORDERING. WHEN YOU ORDER A PART, PLEASE REFER TO PARTS LIST.

# NV-FJ780B POWER SUPPLY/RF SECTION SCHEMATIC DIAGRAM

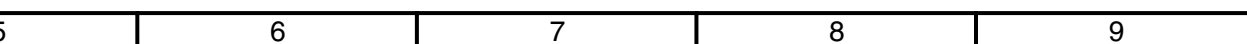
### 8.3. TIMER SECTION IN MAIN SCHEMATIC DIAGRAM





USE PART NUMBER SHOWN ON THIS SCHEMATIC DIAGRAM FOR  
ING.WHEN YOU ORDER A PART,PLEASE REFER TO PARTS LIST.

NV-FJ780B  
TIMER SECTION SCHEMATIC DIAGRAM





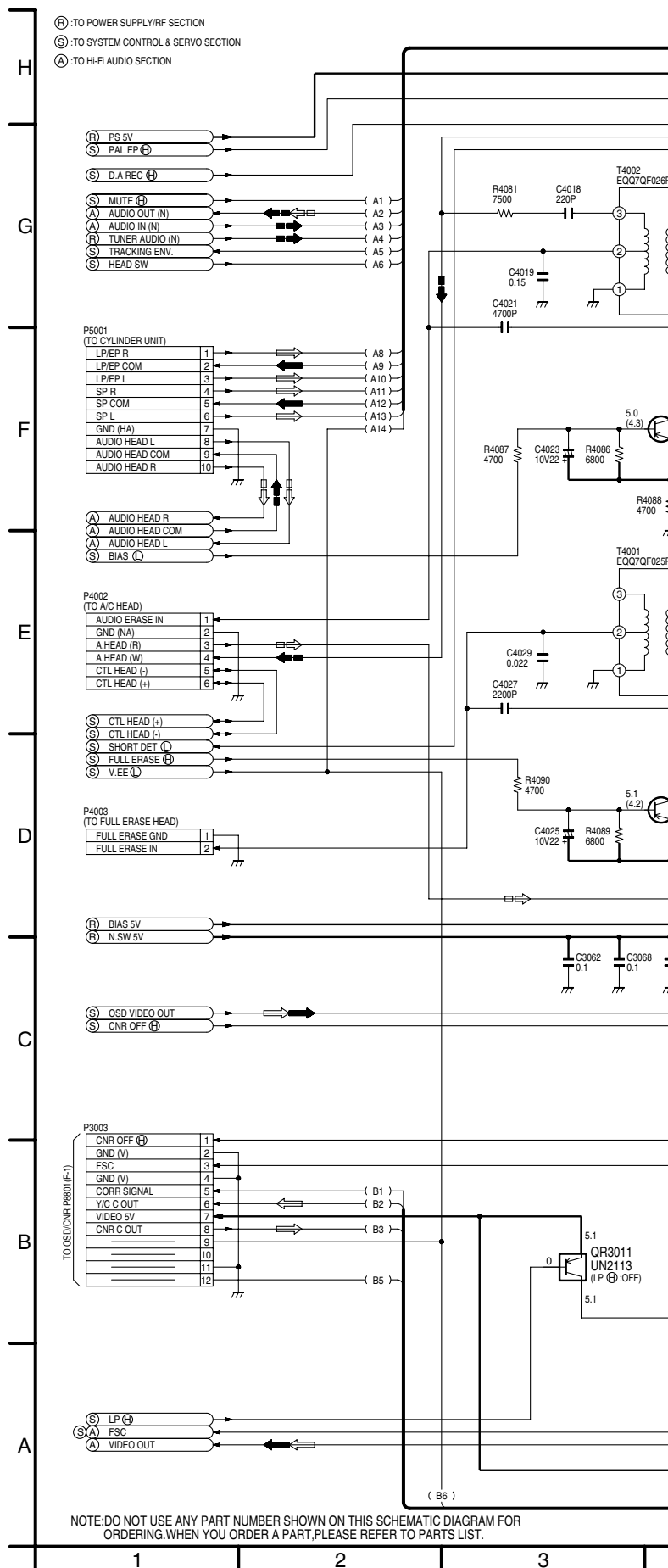




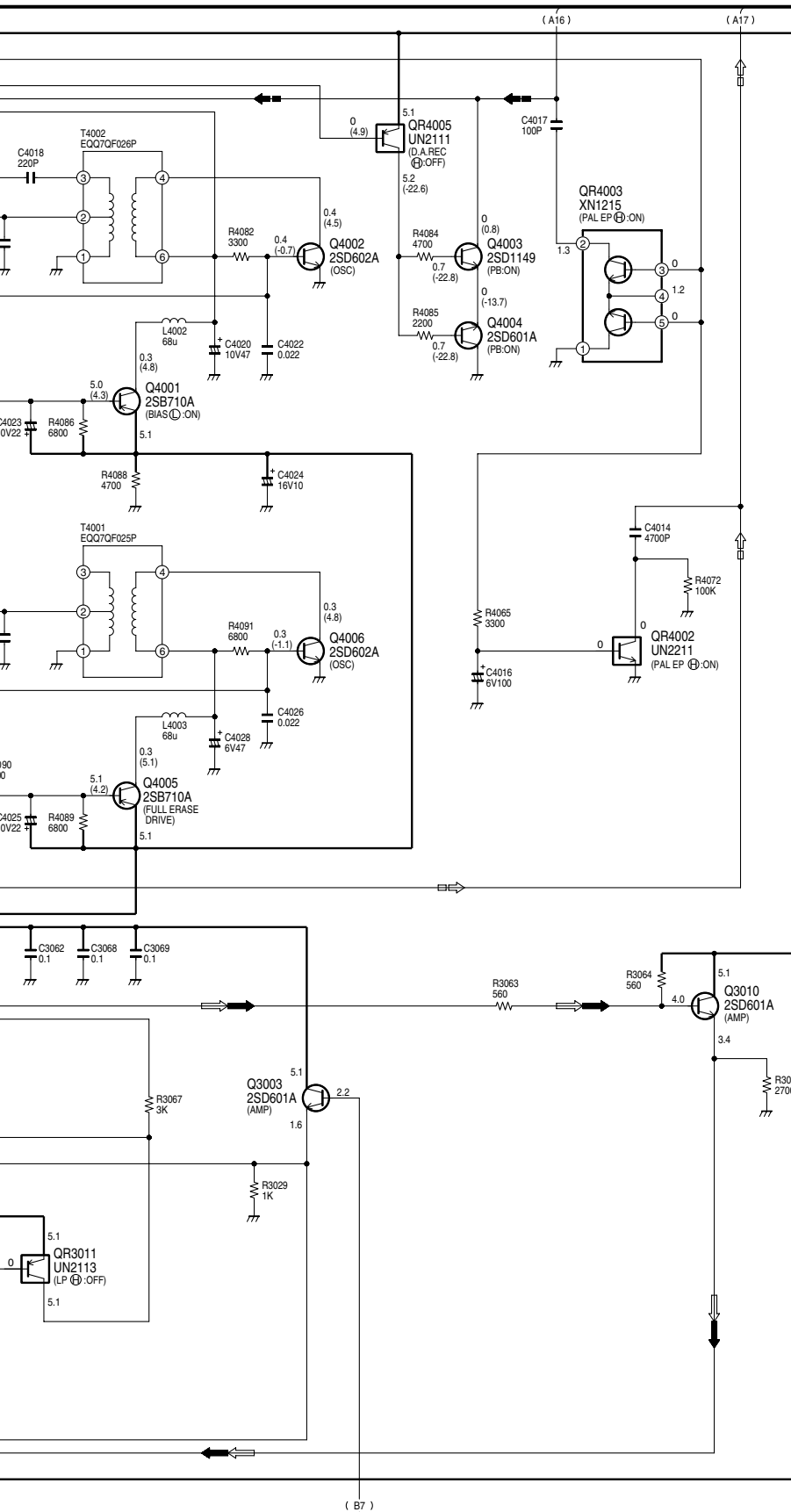




## 8.5. LUMINANCE & CHROMINANCE/ AUDIO SECTION IN MAIN SCHEMATIC DIAGRAM



# C DIAGRAM

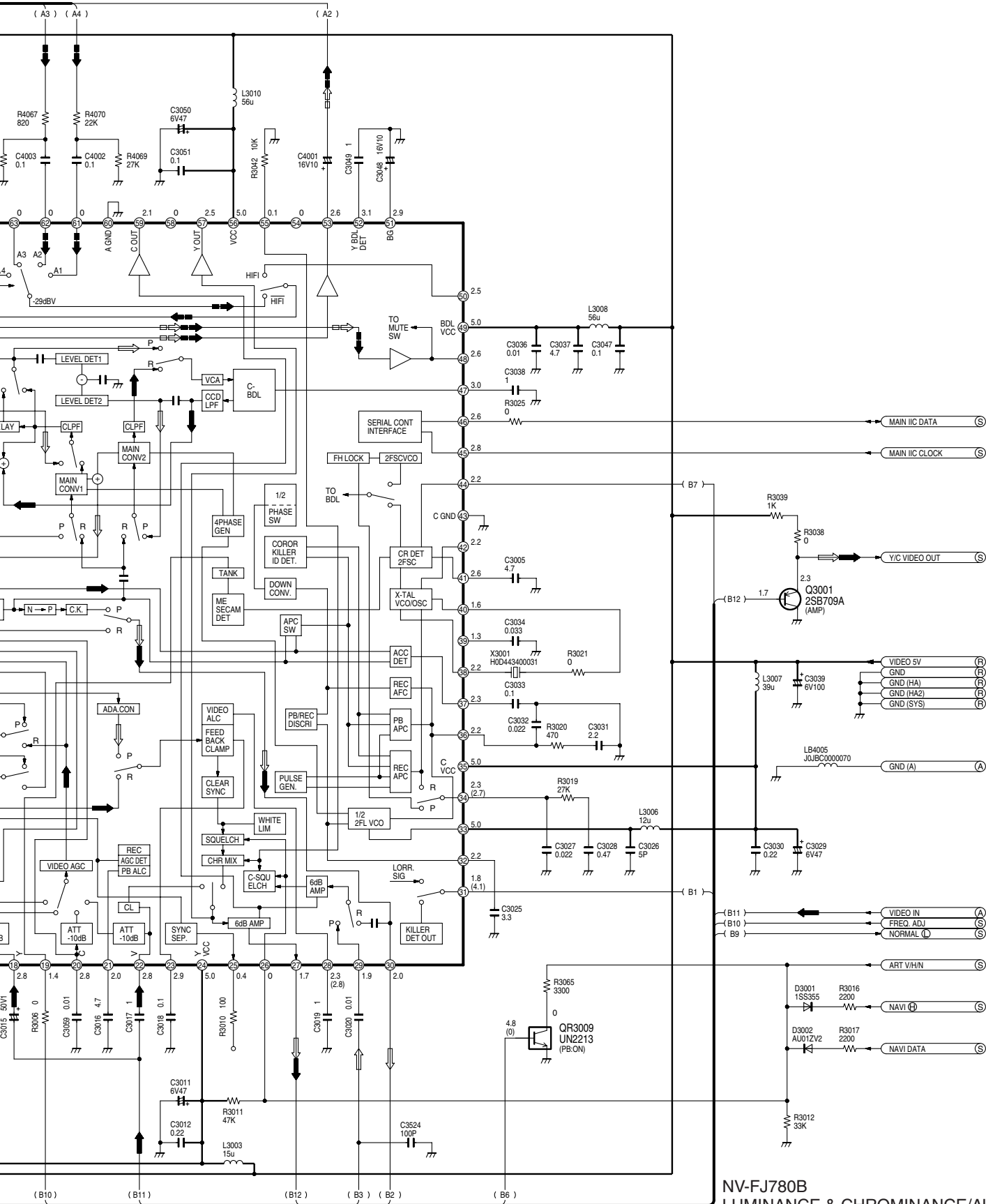


NV-FJ780B LUMINANCE & CHROMINANCE/AUDIO SECTION SCHEMATIC DIAGRAM



← AUDIO MAIN SIGNAL PATH IN REC MODE

← AUDIO MAIN SIGNAL PATH IN PLAYBACK MODE



15

16

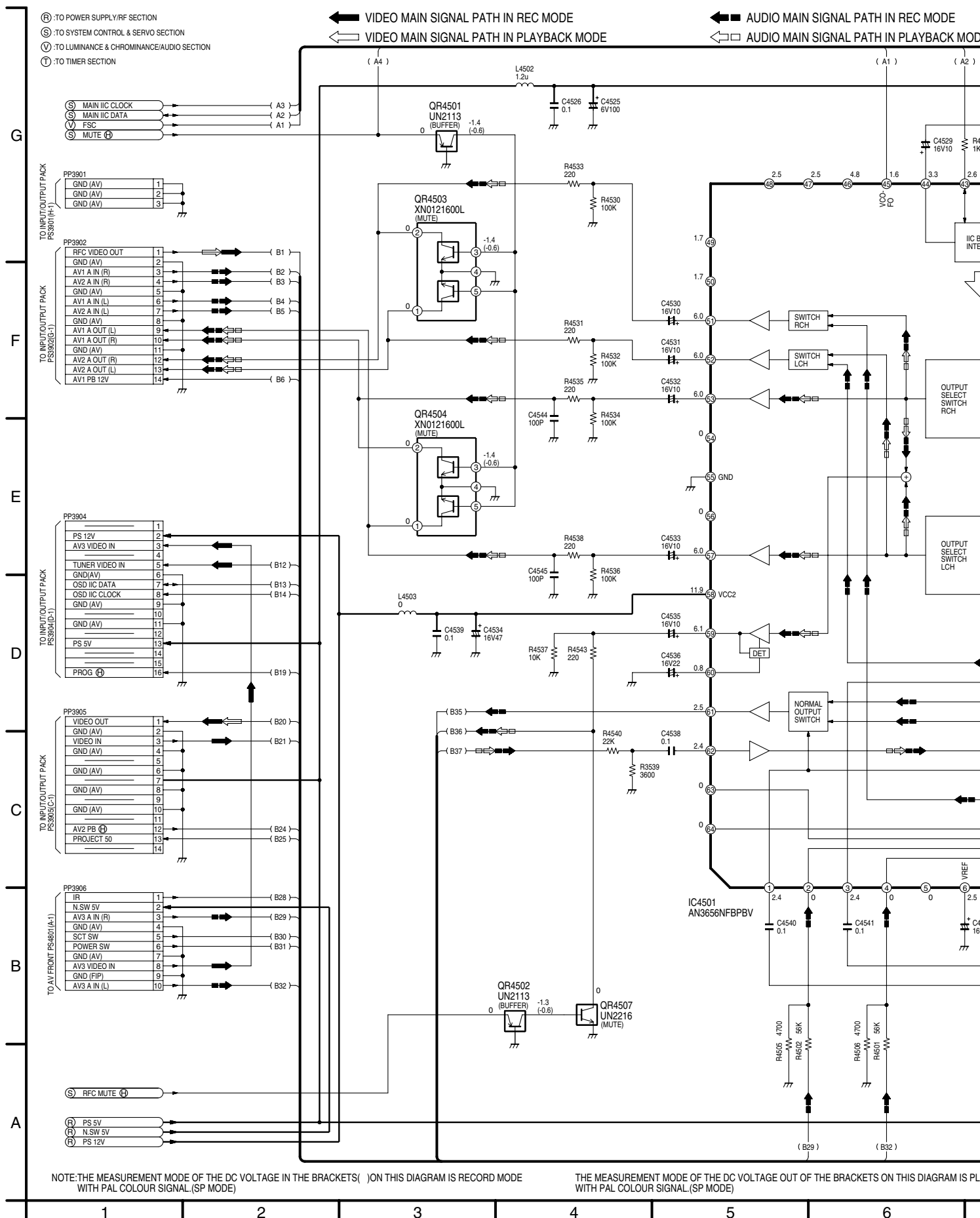
17

18

19

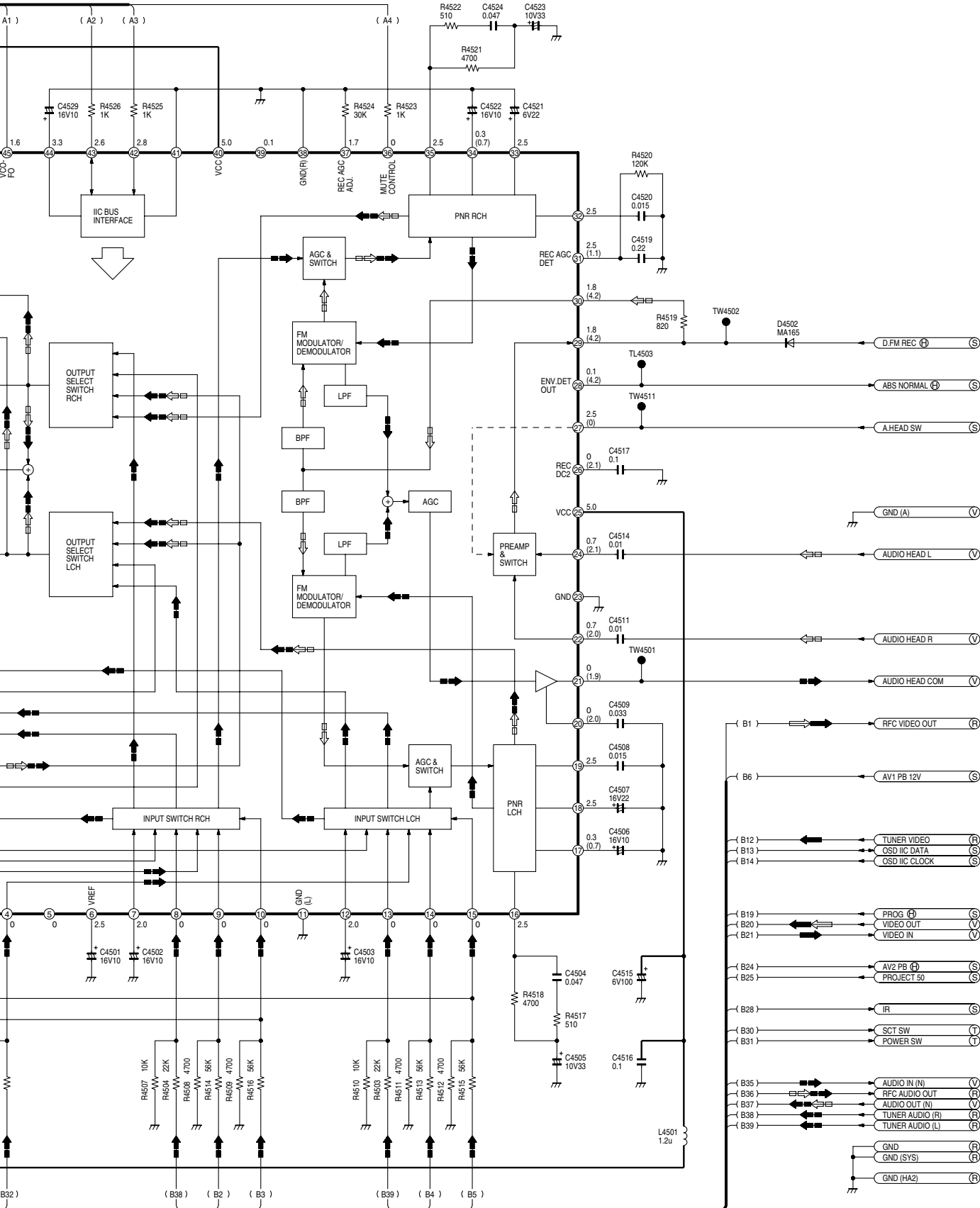
20

## 8.6. Hi-Fi AUDIO SECTION IN MAIN SCHEMATIC DIAGRAM



IN REC MODE

IN PLAYBACK MODE



NV-FJ780B

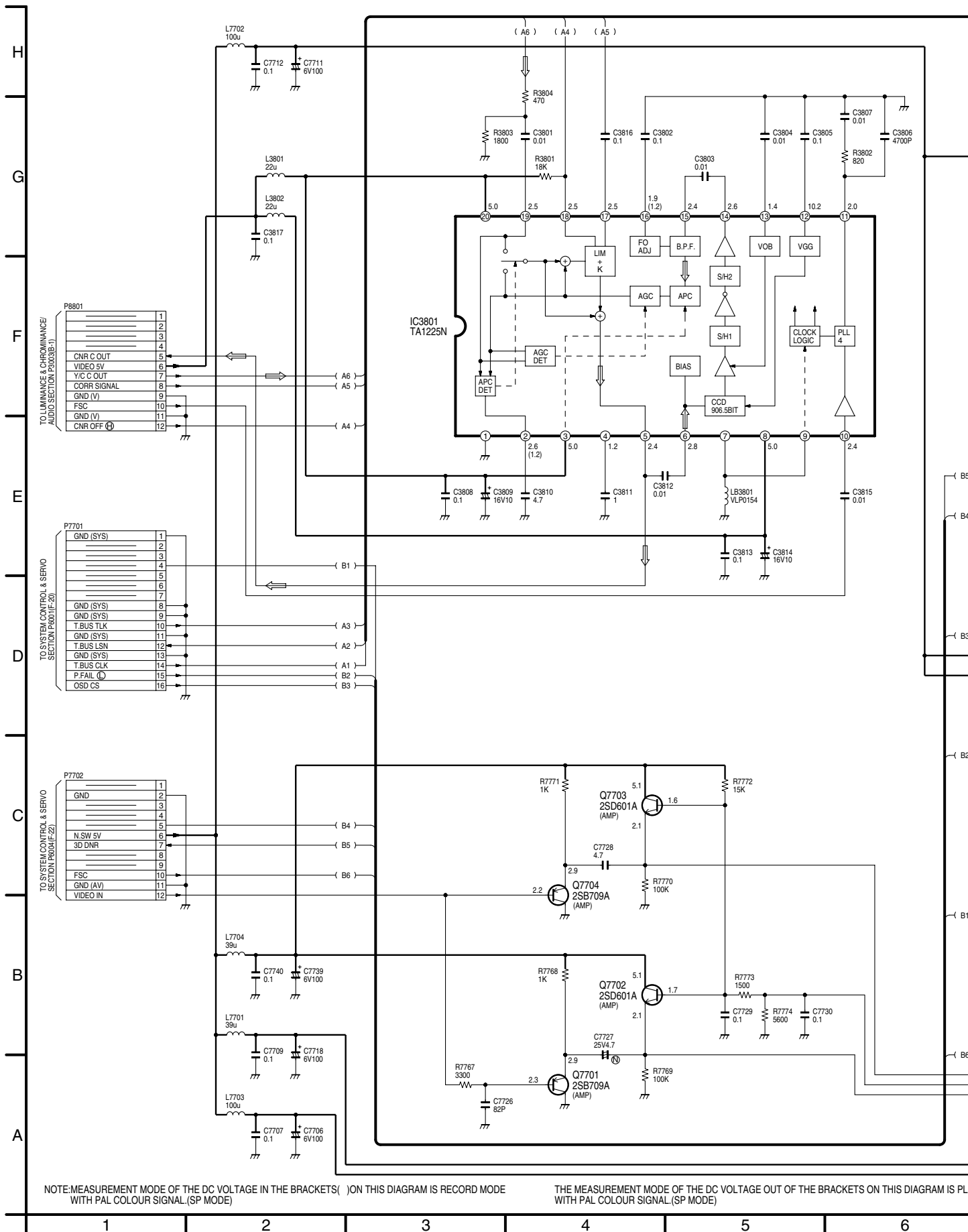
Hi-Fi AUDIO SECTION SCHEMATIC DIAGRAM

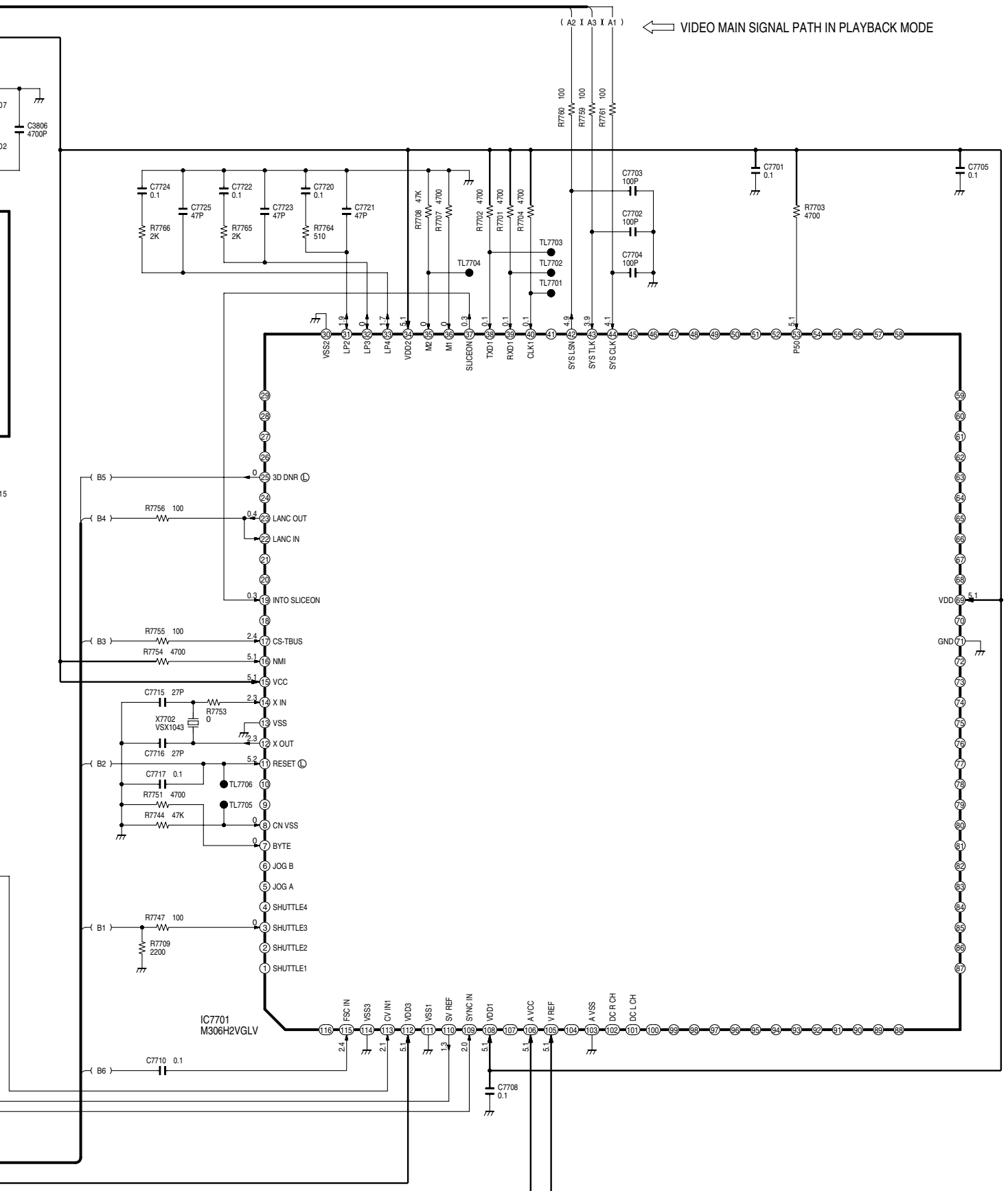
ON THIS DIAGRAM IS PLAYBACK MODE

6 7 8 9 10 11

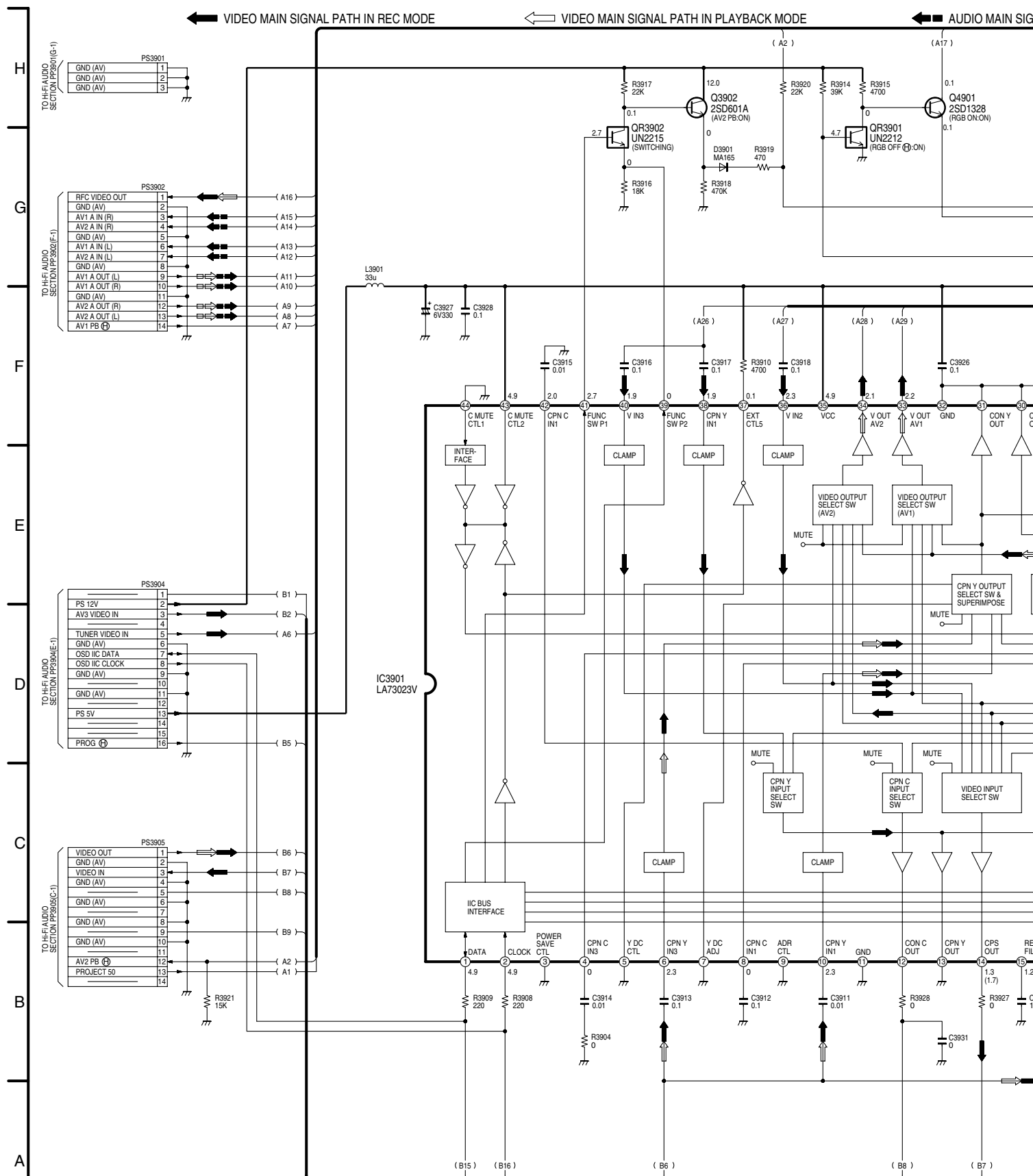


## 8.7. OSD/CNR SCHEMATIC DIAGRAM





## 8.8. INPUT/ OUTPUT PACK SCHEMATIC DIAGRAM

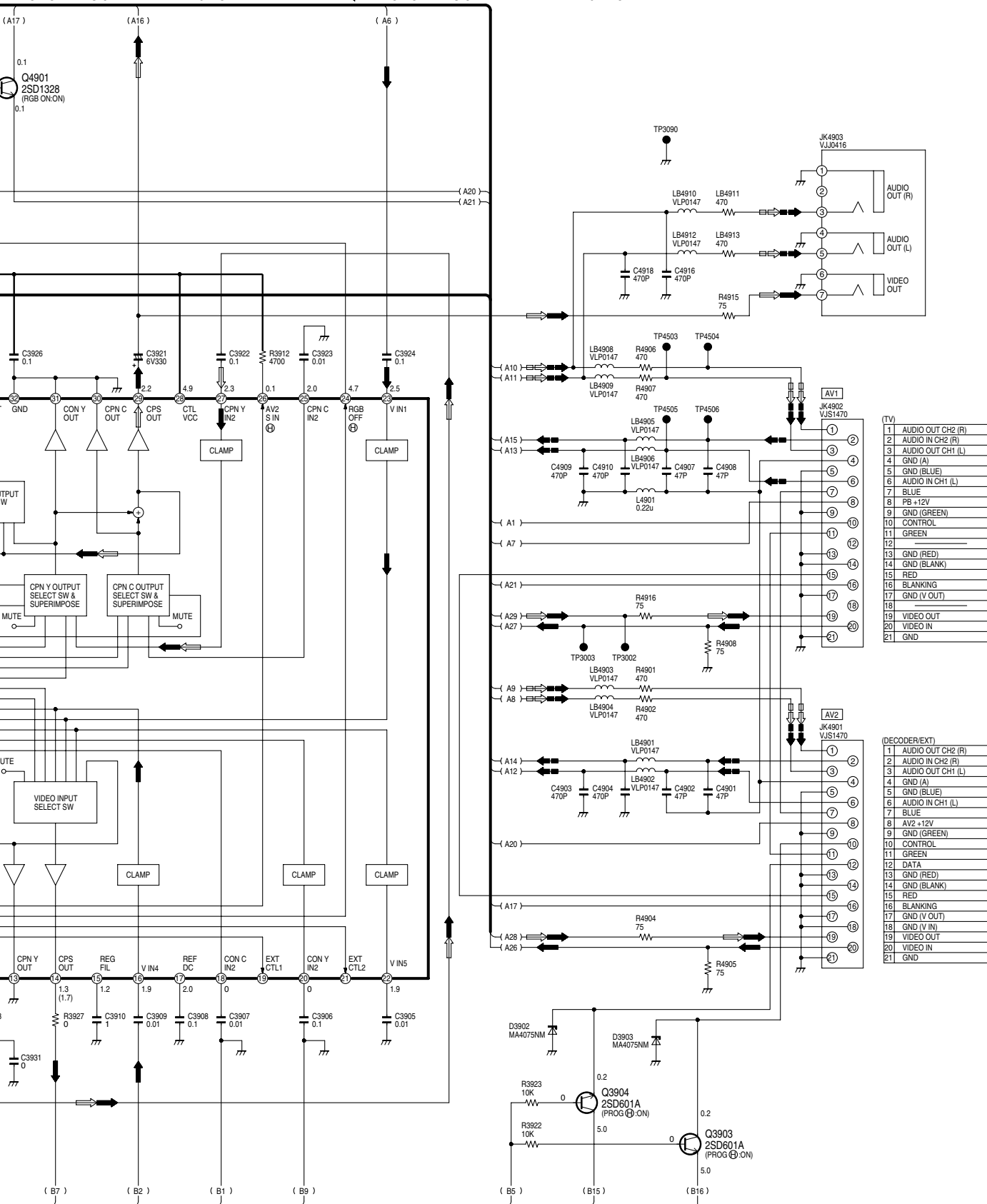


NOTE: THE MEASUREMENT MODE OF THE DC VOLTAGE IN THE BRACKETS ( ) ON THIS DIAGRAM IS RECORD MODE WITH PAL COLOUR SIGNAL (SP MODE)

THE MEASUREMENT MODE OF THE DC VOLTAGE OUT OF THE BRACKETS ON THIS DIAGRAM IS PLAYBACK MODE WITH PAL COLOUR SIGNAL (SP MODE)

■ AUDIO MAIN SIGNAL PATH IN REC MODE

◀▶ AUDIO MAIN SIGNAL PATH IN PLAYBACK MODE



AGRAM IS PLAYBACK MODE

NOTE:DO NOT ANY PART NUMBER SHOWN ON THIS SCHEMATIC DIAGRAM FOR ORDERING.WHEN YOU ORDER A PART,PLEASE REFER TO PARTS LIST.

NV-FJ780B  
INPUT/OUTPUT PACK  
SCHEMATIC DIAGRAM

7

8

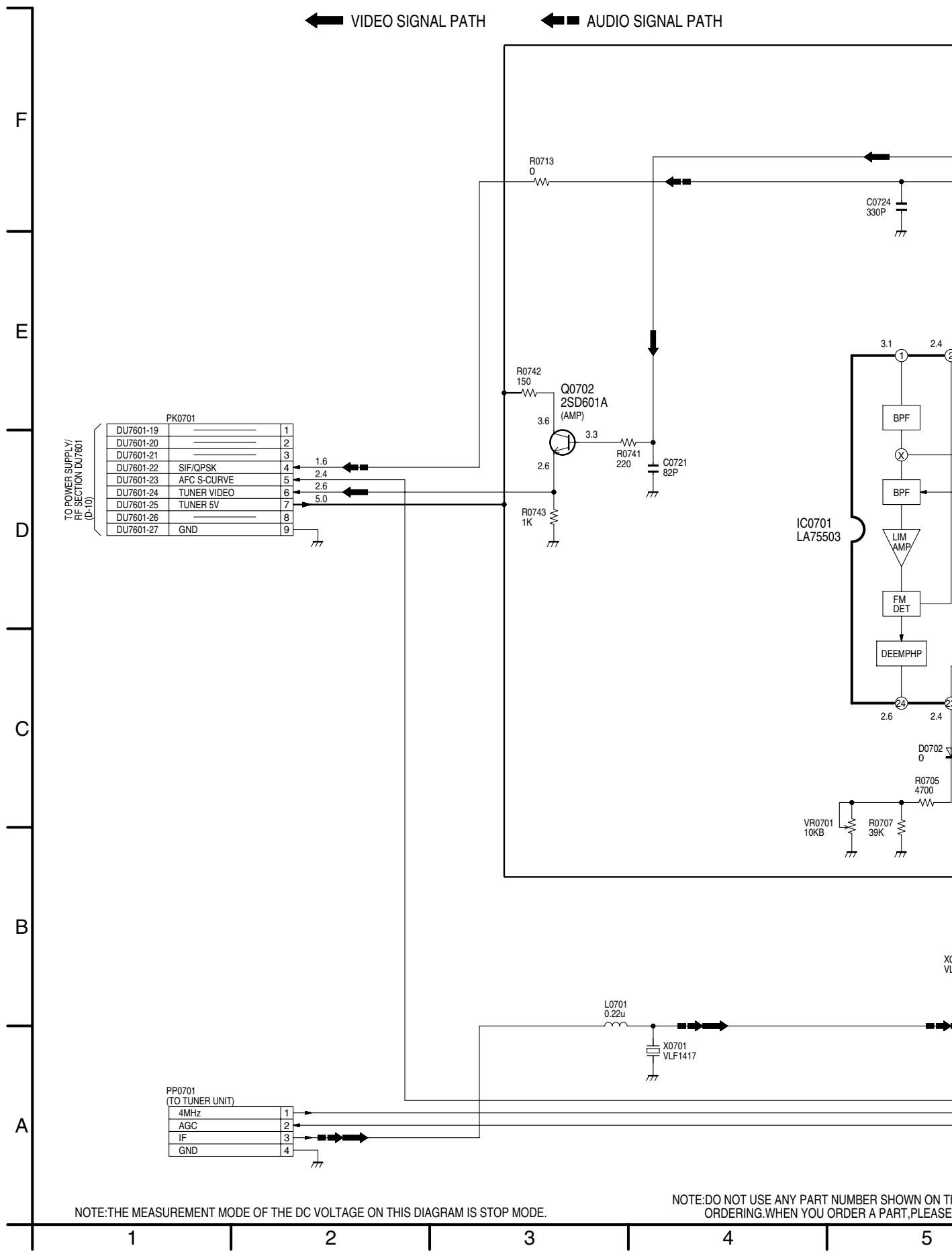
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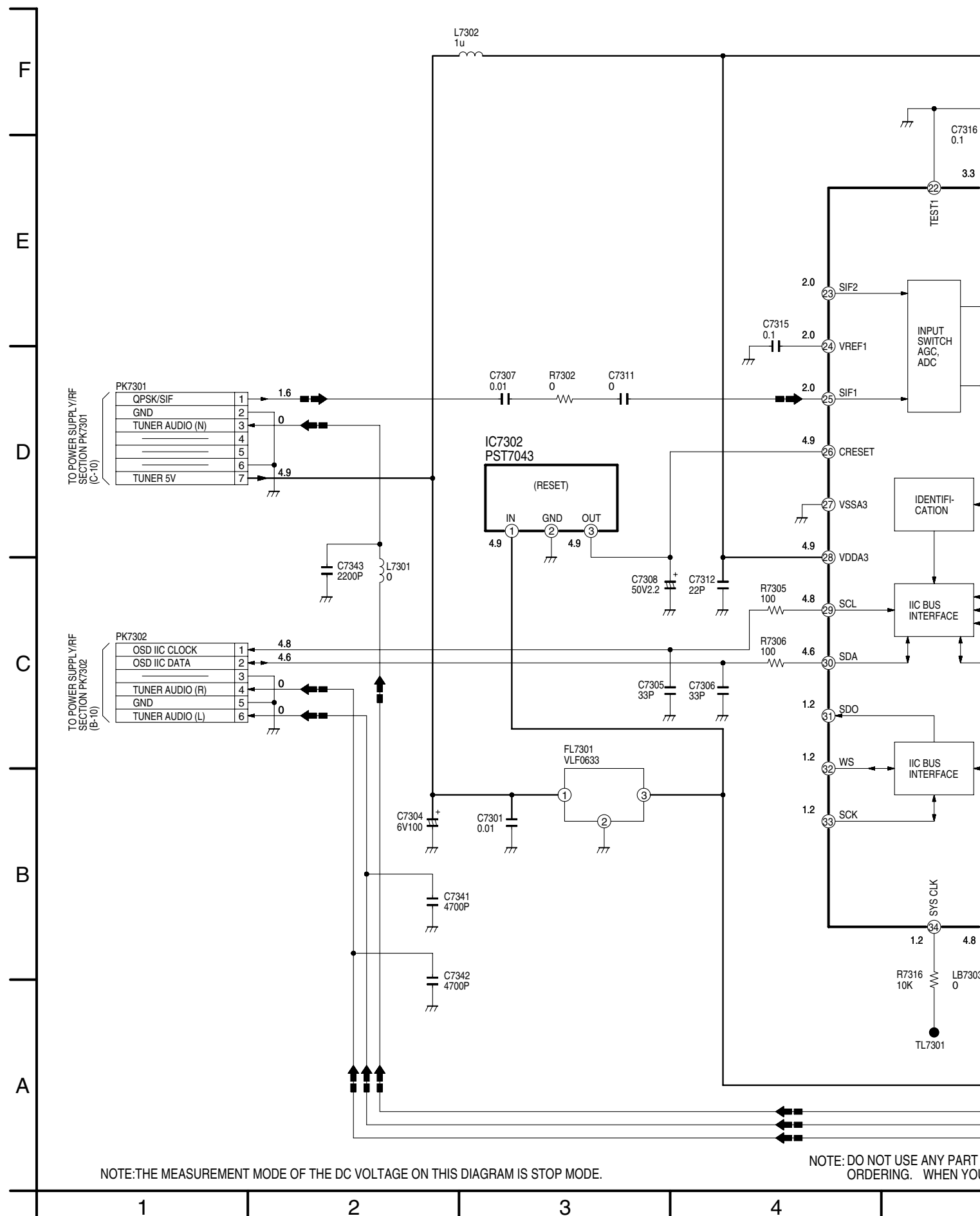
12

# 8.9. TV DEMODULATOR PACK SCHEMATIC DIAGRAM





## 8.10. NICAM DECODER PACK SCHEMATIC DIAGRAM



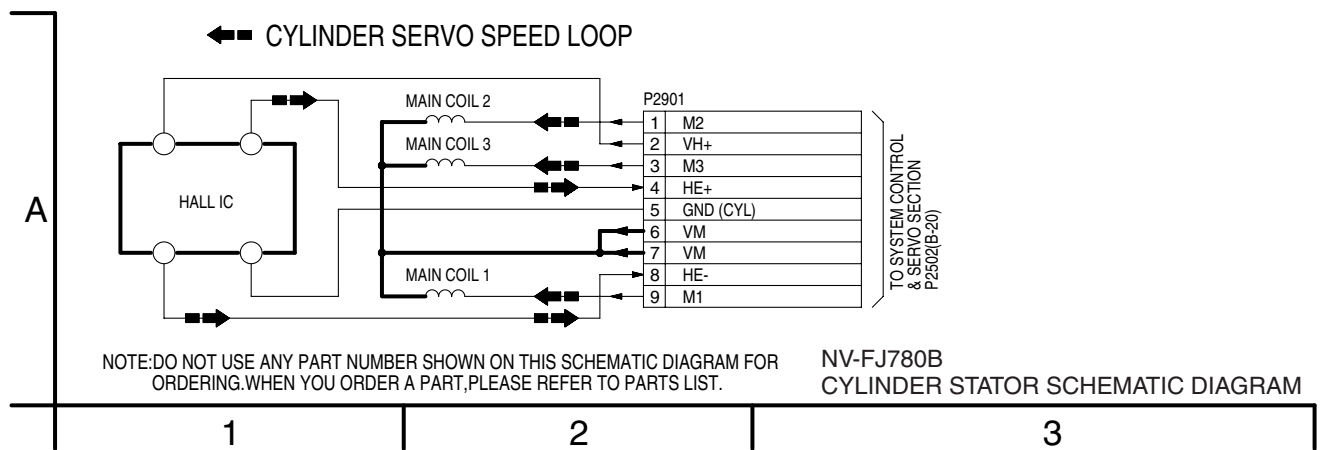
NOTE: THE MEASUREMENT MODE OF THE DC VOLTAGE ON THIS DIAGRAM IS STOP MODE.

NOTE: DO NOT USE ANY PART ORDERING. WHEN YOU

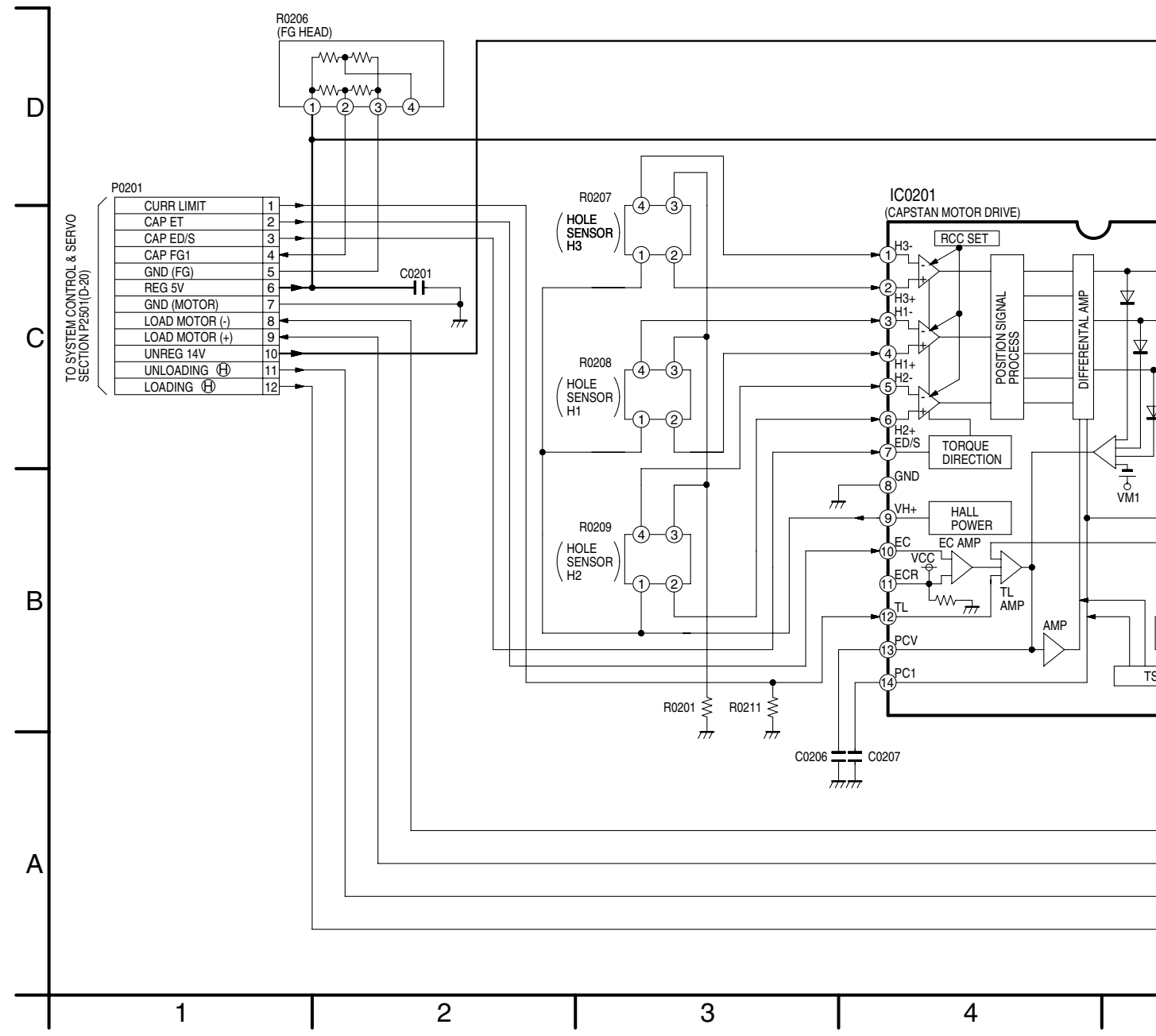
NV-FJ780B  
NICAM DECODER PACK SCHEMATIC DIAGRAM

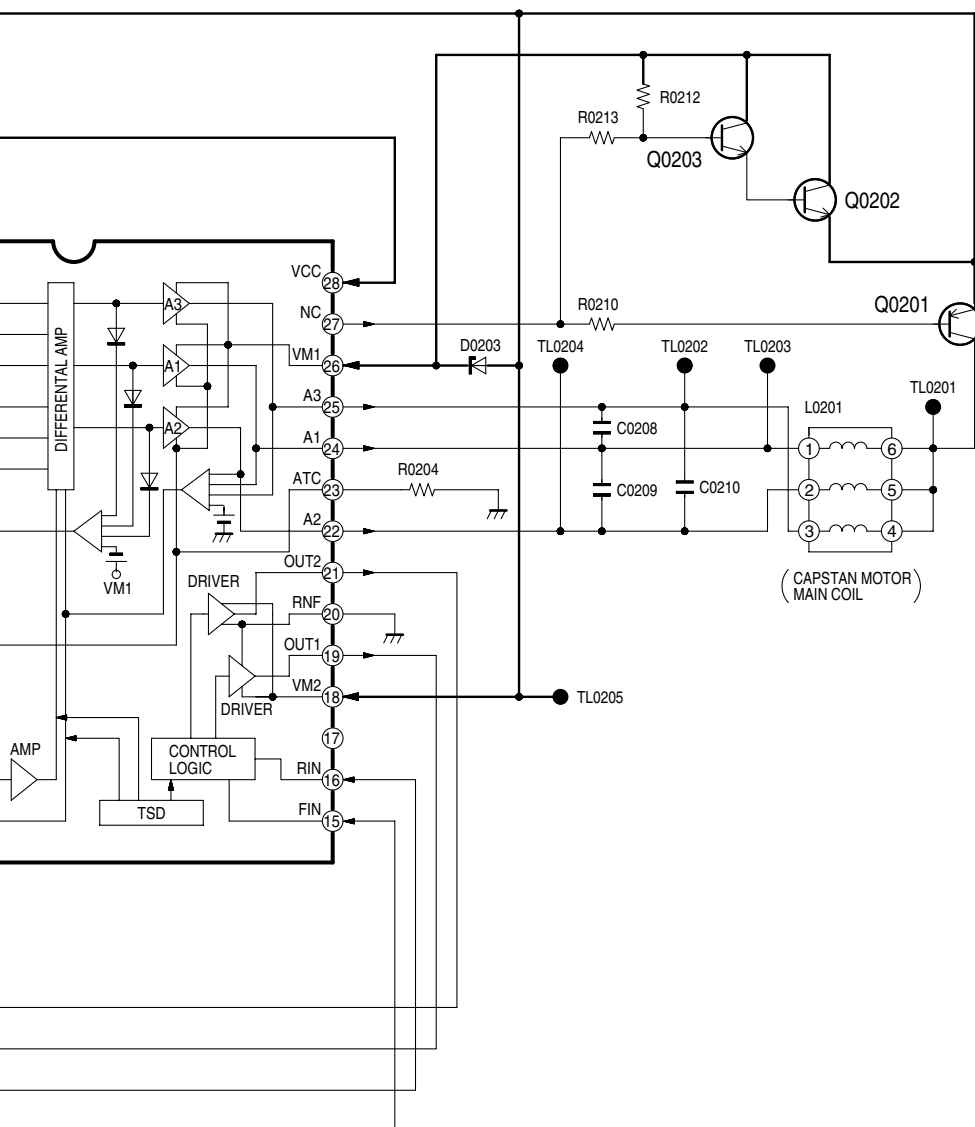


## 8.11. CYLINDER STATOR UNIT SCHEMATIC DIAGRAM



## 8.12. CAPSTAN UNIT SCHEMATIC DIAGRAM





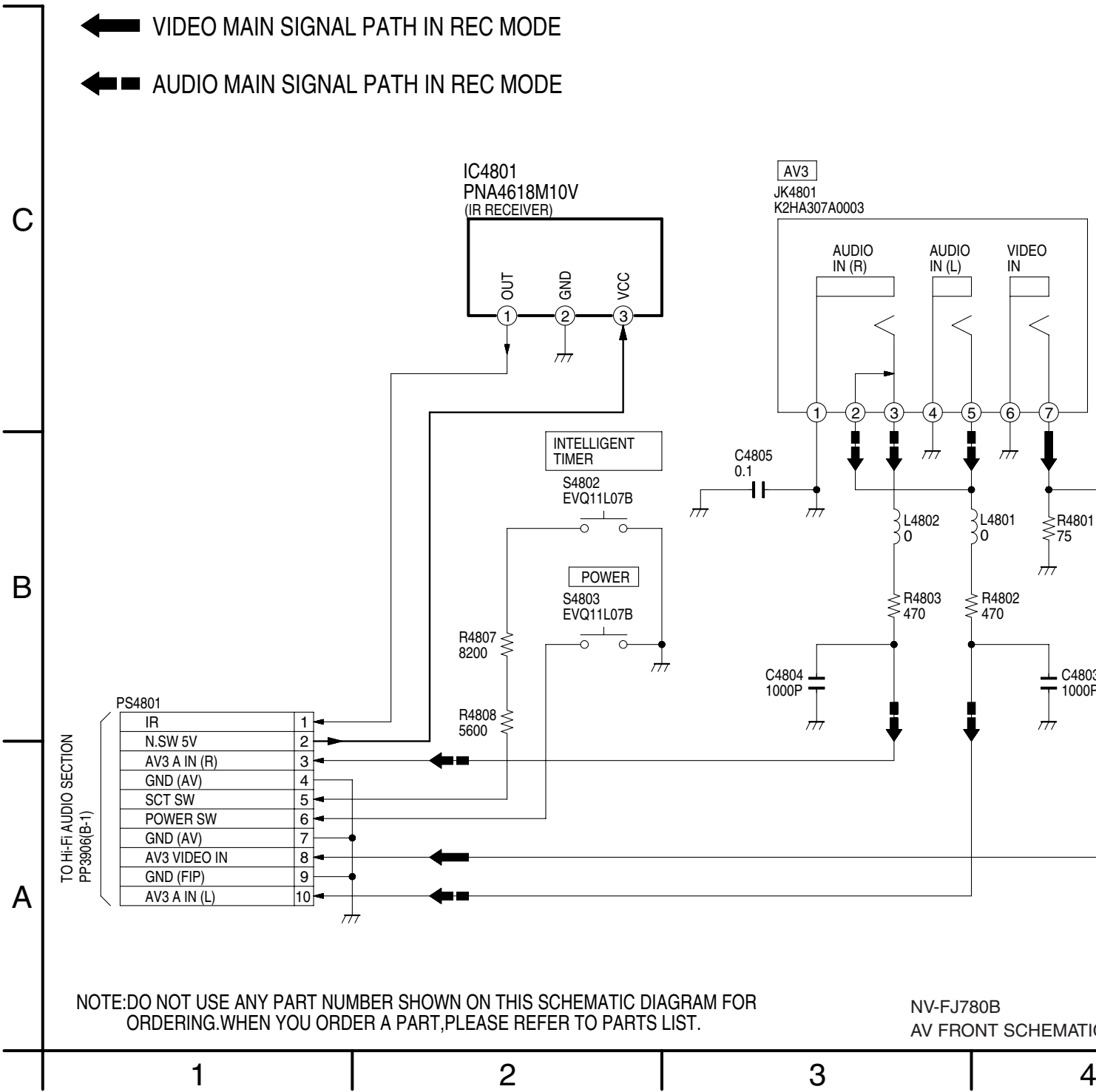
NV-FJ780B  
CAPSTAN UNIT SCHEMATIC DIAGRAM

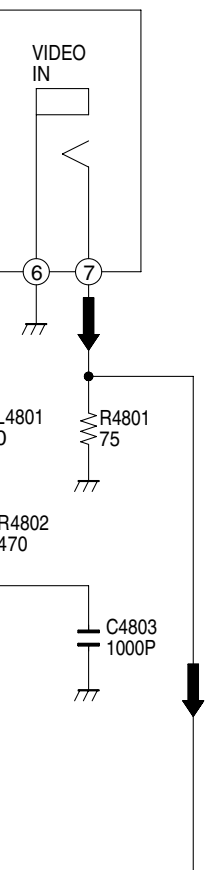
5

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8.13. AV FRONT SCHEMATIC DIAGRAM





DB  
IT SCHEMATIC DIAGRAM

4